

PATENT
29925/39913

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Da-soon Lee
Serial No.: To be assigned
Filed: April 16, 2004
For: Method for Manufacturing a
Nonvolatile Memory Transistor
Group Art Unit: To be assigned
Examiner: To be assigned

) I hereby certify that this paper and the
) documents referred to as enclosed
) therewith are being deposited with the
) United States Postal Service as Express
) Mail, Airbill No. **EV323780529US**
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) Virginia 22313-1450

) 
) Richard Zimmermann
)
)
)
)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Submitted herewith for consideration by the examiner are copies of the documents identified on the attached Form PTO-1449. Entry and consideration of the submitted documents is solicited.

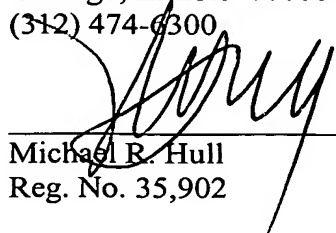
The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 13-2855.

Respectfully submitted,

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April 19, 2003

By:



Michael R. Hull
Reg. No. 35,902

Form PTO-1449 (Modified) INFORMATION DISCLOSURE STATEMENT	U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. 30205/39513	Serial No. To be Assigned
	Applicant Da-soon Lee			
	Filing Date April 19, 2004		Group To be Assigned	

U.S. PATENT DOCUMENTS							
*Examiner Initials		Document Number	Issue Date	Name	Class	Subclass	Filing Date if Appropriate
		5,973,352	10/26/1999	Noble			
		6,238,976	05/29/2001	Noble et al.			

FOREIGN PATENT DOCUMENTS								
*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation	
							Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		

EXAMINER	DATE CONSIDERED
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.	